

Jose Kurian

Hillsboro, OR | 5713556424 | joskus@gmail.com | LinkedIn | www.josekurian.com

PROFESSIONAL SUMMARY

Results-driven **Software Architect with 20+ years of experience** in designing, developing, and deploying scalable enterprise software solutions. **Hands on experience in GenAI solutions, full-stack development, cloud architecture, and DevOps**. Adept at leading & mentoring cross-functional teams to deliver complex software projects on time.

CORE COMPETENCIES

Software Architecture
Cloud Computing (Azure, On Prem)
Generative AI
Virtualization
Containerization
Ul/UX Design
Full-Stack Development
Micro Front Ends
Microservices
Cl/CD Pipelines
Agile & Scrum
DevOps
Technical Leadership
Manufacturing Automation
Workflow Automations
Cybersecurity
Supply Chain Systems

PROFESSIONAL EXPERIENCE

Software System Architect Intel Corporation – Hillsboro, OR 03/2012 – Present

- Currently working in foundry automation team engineering Generative AI solutions & transformative Analytics User Experiences. Architected and implemented automation solutions across Intel's manufacturing, supply chain, and foundry operations, enabling scalable and secure systems.
- Built and deployed user-centric applications using React.js and Python, improving usability and performance across internal platforms. Hands on experience with Micro Front Ends, Micro Services, GitHub Actions, Helm Charts and containerization
- Led the virtualization of industrial machines and manufacturing shop floor lines, reducing cost, improving uptime & operational efficiency. Designed and modernized Manufacturing Execution Systems (MES), semiconductor tape-out workflows, and material build planning systems.
- Defined IT strategy and roadmaps for foundry services, enhancing online customer service portals and data integration. Spearheaded post-silicon application development, ensuring robust system support and seamless manufacturing execution.
- Established secure data architecture and access controls for manufacturing systems in alignment with foundry product introduction requirements.

Senior Software Engineer REI Systems – Herndon, VA 06/2011 – 03/2012

• Contributed to a platform framework for rapid application development and implementation for various customers of REI Systems Inc. This common framework is used by federal agencies so that US Web Design Standards are applied as well applications can be delivered quickly.

Technology lead Infosys – Bangalore, India 09/2007 – 10/2011 • Delivered capabilities for various clients of Infosys. Job responsibilities involved Client Interfacing, Requirement Analysis, Solution designing, application designing, technical architecture designs, Web designing, Coding, Unit testing, Team leading.

Module Lead

Wipro Technologies – Bangalore, India

09/2005 - 09/2007

 Delivered capabilities for various clients of Wipro Technologies. Job responsibilities involved Client Interfacing, Requirement Analysis, Solution designing, application designing, technical architecture designs, Web designing, Coding, Unit testing, Team leading.

EDUCATION

- Master's Degree, Computer Applications Bharathiar University 06/2001 – 06/2004
- Bachelor's Degree, Chemistry Mahatma Gandhi University 06/1997 – 06/2001

TECHNICAL SKILLS

- Languages: C#, Python, JavaScript
- Frameworks: React, Node.js, TypeScript, .NET Core, ASP.Net, Yarp, Articul8 & Azure LLMs, RAG
- Tools: Docker, Kubernetes, Git, VMware
- Cloud: Azure, On Premise VMware and Kubernetes
- Databases: MS SQL, Oracle, PostgreSQL, MongoDB, MySQL, NoSQL
- Methodologies: Agile, DevOps GitHub Actions, Helm, TDD, Object Oriented, Design Patterns

PROJECTS

- Currently working in foundry automation team engineering Generative AI solutions & transformative Analytics User Experiences. Architected, designed and deployed external/internal and inside the factory facing solutions at intel
- Functioned as the cyber security lead & application portfolio owner for manufacturing automation team
- Contributed to Optane memory manufacturing & path finding in virtualization at intel's data centers. Enabled Tapeout workflow automation for memory silicon at intel
- Re-architected & delivered myfoundry.intel.com & ifs.intel.com to improve operational efficiency, usability & product life cycle management
- Realized several complex automations for post silicon processing stages for silicon product engineering via a new framework\architecture. Enabled various debuggers, test programs, Q&R calculators for silicon chip health for intel products
- Worked on the core platform framework used by federal agencies enforcing US web design standards and reducing time to enable solutions from 4 weeks to 3 days
- Designed and developed FAST Services Generic middle tier for search.intel.com
- Engineered Intel IT Center Resource library user interface with plug & play context aware widgets for intel.com
- Architected scalable, highly usable integrated e-commerce portal for Canadian Pacific Railway
- Automated and simplified systems integrations for Canadian Pacific Railway, SunTrust, GlaxoSmithKline etc.